

10/039,541

5. (Currently Amended): A system level device for battery and integrated circuit chip integration comprising:

at least one battery;

at least one integrated circuit chip powered by said at least one battery; and

a package connected to any of said at least one battery and said at least one integrated circuit chip,

wherein said at least one battery connects to a pair of opposed upright ends of said package, and

wherein said at least one integrated circuit chip is disposed between said at least one battery and said package[.], and

wherein said at least one integrated circuit chip lays on top of a portion of said package.

6. (Original): The device of claim 3, wherein said at least one battery further comprises a stack of connected batteries.

7. (Currently Amended): A system level device for battery and integrated circuit chip integration comprising a multi-chip module integration system, wherein said multi-chip module integration system comprises:

a multi-chip module having a pair of opposed upright ends;

at least one battery connected to said multi-chip module; and